

## **ECEN 326**

### **Spring 2022 Lab Policy**

Section 501: Wednesday 8:00 AM - 10:50 PM ZACH 324

Section 502: Thursday 11:10 AM – 2:00 PM ZACH 324

Section 503: Thursday 5:30 PM - 8:20 PM ZACH 324

#### **TA: Srujan Kumar Kaile**

Office: Zoom

Office Hours: T 12:00 PM - 2:00 PM

Email: srujankaile@tamu.edu

#### **Grading:**

##### **Demonstration 25%**

- One demonstration per person
- Total points are divided among demonstration items
- Demonstration is required in order to receive any credit for the lab

##### **Lab Reports 75%**

- One report per person
- Due at the beginning of the lab session via Canvas, but may be updated until Thursday 11:59PM with TA approval.
- Total points for calculations (25%) are divided among calculation items
- Total points for simulations (25%) are divided among simulation items and proper labeling of schematics and graphs
- Total points for measurements (25%) are divided among measurement items and proper labeling of schematics and graphs as well as conclusion and comparison
- **No late reports will be accepted** without any university-approved excuse
- **All sections** need to be completed in order to receive any credit for the lab

#### **Policies**

- Students are expected to arrive **on time**.
- If for any reason the student cannot arrive on time (such as back-to-back classes), then they are to notify the TA **before the first lab**.
- Makeup demonstrations are allowed under excused absences, and excused absences need to be reported to the TA via email with the instructor CC'd.
- Collaboration is encouraged, but copying directly is strictly prohibited, and violators will be reported for academic dishonesty.
- Students are welcome to see either TA during their office hours.
- Students will have their name and UIN written on the side of their breadboard and their student ID in hand for the demonstration.
- Breadboards and circuits cannot be shared, and any attempt to do so will be reported as academic dishonesty. Sharing the AD2 is perfectly fine.
- Students ready to demonstrate will write their names on the board and wait to be called on, first come first serve.
- Names written on the board will be **only** of those students ready to demonstrate.

- Students who write their name on the board but are not ready to demonstrate will lose five points from their demonstration grade.
- Calculations may be handwritten, but they need to be legible and understandable.
- Calculations must be:
  - Clean sheet of paper (do not use paper that has been previously used for printing unrelated text)
  - Clear handwriting (consider typing the text part of your pre-labs)
  - Follow the same components labeling as in the lab manual
  - Boxed final results
- Illegible work will not receive any credit.
- Reports must be **typed**.
- Plots/pictures need to have captions, axis names, marks, and annotations if needed.
- Make sure all plots/schematics have **light** background by inverting color if necessary.
- Plots/schematics with dark background will not receive any credit.
- **No food or drink in the lab.**

### **Aggie Code of Honor**

*“An Aggie does not lie, cheat, or steal, or tolerate those who do.”*

Honor Council Rules and Procedures: <http://www.tamu.edu/aggiehonor>

## ECEN 326 Lab Schedule, Spring 2022

Location: ZACH 324

TA: **Srujan Kumar Kaile** ([srujankaile@tamu.edu](mailto:srujankaile@tamu.edu))

Office Hours: T 12:00PM - 2:00PM

Lab No.	Lab Time	Report Due	Topic
0	Sec.501 1/26 W 8:00AM-10:50PM	-	Orientation
	Sec 502 1/27 R 11:10PM-2:00PM		
	Sec 503 1/27 R 5:30PM-8:20PM		
1	Sec.501 2/2 W 8:00AM-10:50PM	Beginning of lab	Design of a Common-Emitter BJT Amplifier
	Sec 502 2/3 R 11:10PM-2:00PM		
	Sec 503 2/3 R 5:30PM-8:20PM		
2	Sec.501 2/9 W 8:00AM-10:50PM	Beginning of lab	Design of a Three-Stage BJT Amplifier
	Sec 502 2/10 R 11:10PM-2:00PM		
	Sec 503 2/10 R 5:30PM-8:20PM		
3	Sec.501 2/16 W 8:00AM-10:50PM	Beginning of lab	Design of a Two-Stage MOSFET Amplifier
	Sec 502 2/17 R 11:10PM-2:00PM		
	Sec 503 2/17 R 5:30PM-8:20PM		
4	Sec.501 2/23 W 8:00AM-10:50PM	Beginning of lab	Design of a BJT Differential Amplifier
	Sec 502 2/24 R 11:10PM-2:00PM		
	Sec 503 2/24 R 5:30PM-8:20PM		
5	Sec.501 3/2 W 8:00AM-10:50PM	Beginning of lab	Design of a MOS Differential Amplifier
	Sec 502 3/3 R 11:10PM-2:00PM		
	Sec 503 3/3 R 5:30PM-8:20PM		
6	Sec.501 3/9 W 8:00AM-10:50PM	Beginning of lab	Design of Current Mirrors
	Sec 502 3/10 R 11:10PM-2:00PM		
	Sec 503 3/10 R 5:30PM-8:20PM		
7	Sec.501 3/23 W 8:00AM-10:50PM	Beginning of lab	Design of a BJT Operational Transconductance Amplifier
	Sec 502 3/24 R 11:10PM-2:00PM		
	Sec 503 3/24 R 5:30PM-8:20PM		
8	Sec.501 3/30 W 8:00AM-10:50PM	Beginning of lab	Frequency Response of a Common-Emitter BJT Amplifier
	Sec 502 3/31 R 11:10PM-2:00PM		
	Sec 503 3/31 R 5:30PM-8:20PM		
9	Sec.501 4/6 W 8:00AM-10:50PM	Beginning of lab	Frequency Response of a Cascode BJT Amplifier
	Sec 502 4/7 R 11:10PM-2:00PM		
	Sec 503 4/7 R 5:30PM-8:20PM		
10	Sec.501 4/13 W 8:00AM-10:50PM	Beginning of lab	Design of a BJT Shunt-Series Feedback Amplifier
	Sec 502 4/14 R 11:10PM-2:00PM		
	Sec 503 4/14 R 5:30PM-8:20PM		
11	Sec.501 4/27 W 8:00AM-10:50PM	Beginning of lab	Design of a Two-Stage Amplifier with Miller Compensation
	Sec 502 4/28 R 11:10PM-2:00PM		
	Sec 503 4/28 R 5:30PM-8:20PM		