Mixers II



As we have seen before mixers* play an important role in communication systems. On the transmitter side they perform up-conversion and on the receiver side they down-convert. We will focus mainly on down-conversion mixers.

In this section, we will discuss:

- mixer fundamentals
- mixer metrics
- mixer implementations in Bipolar and CMOS

* This Part 2 material on mixers is mainly contributed by Dr. S. H. K. Embabi's previous notes

Mixer Fundamentals

A mixer is expected to produce signals with spectral components which do not exist in the input signal (a 900MHz mixed with a 800MHz produces components at 100MHz and 1700MHz). Only *nonlinear* or *time-varying* devices can perform such a function.

Most mixer implementations use some kind of a *multiplication* of two signals, the signal to be down-converted (*RFI* or *IF*) and the signal whose frequency determines the output frequency (LO). If we assume that



The desired spectral component can be acquired by filtering the sun term. The amplitude of the product (IF) is proportional to the *RF* signal if the amplitude of the LO is constant.

Mixer Metrics

Conversion Gain

The conversion gain may be measured as *voltage* or *power gain*. The *voltage conversion gain* of a mixer is defined as follows:

 $Voltage \ Conversion \ Ganin = \frac{rms \ voltage \ of \ the \ IF \ signal}{rms \ voltage \ of \ the \ RF \ signal}$

The *power conversion gain* of a mixer is defined as follows:

 $PowerConversion(transducer)Gain = \frac{the IF \ power \ delivered to the \ load}{rms \ available \ RF \ power \ from the \ source}$

If the load and source impedance are equal, the voltage and power conversion gains are equal in dB. In the case where the mixer is driven by a filter (image reject filter) the input impedance of the mixer must provide proper termination (50 Ω) to the filter otherwise the filter will exhibit ripples. The mixer's output sees the IF filter, which may be passive. The input impedance of such a passive filter is typically not equal to 50 Ω . Therefore, the source impedance is different than the load impedance the voltage and power conversion gain would be different.

Port-to-Port Isolation

Signals may leak through different mechanisms from one port to the other.

We may have:

- LO-to-RF leakage, which causes self-mixing (problem for zero-IF). The LO leakage may even reach the antenna through the LNA (recall the LNA has nonzero return "gain").
- RF-to-LO feedthrough allows interferers and spurs present in the RF signal to interact with the LO.
- feedthrough may cause desensitization of consequent blocks (remember that • LO-to-IF the LO power may be greater than that of the desired IF signal).
- RF-to-IF feedthrough causes problems in some architectures such as zero-IF because of the leakage of low-frequency even-order intermod. Products (even-order distortion).

Either the mixer's isolation must be good enough or the surrounding components must be tolerant to leakage. Linearity 4

Mixer Implementations

As mentioned before, mixers use multiplication to achieve frequency translation. Multiplication can be implemented either directly, using a multiplier circuit, or indirectly, using a nonlinear circuit.

Let us consider the second option first.

Mixers using Nonlinear Circuits

Consider a nonlinear circuit with the following input-output relation:

$$v_{out} = \mathbf{a}_0 + \mathbf{a}_1 v_{in} + \mathbf{a}_2 v_{in}^2 + \mathbf{a}_3 v_{in}^3 + \mathbf{a}_4 v_{in}^4 + \cdots$$

If v_{in} is the sum of the *RF* and the *LO* signals, the output would have spectral components at:

• DC (resulting from the *even*-order nonlinear terms)

much smaller than IF and can thus be filtered out

- Harmonics of the RF $(m\mathbf{w}_{RF})$ and harmonics of the LO $(m\mathbf{w}_{LO})$
 - *much higher than IF and is filtered out* (example: RF=900MHz and LO=830MHz, hence IF=70MHz, while harmonics of RF are at above 900MHz and those of the LO are above 800 MHz)
- Intermodulation Products $(p\mathbf{w}_{RF} \pm q_{LO})$. The desired IF is intermod. Product with q=p=1. The rest are undesired. Unfortunately, some of them are too close to the desired IF to be selectively suppressed through filtering. One needs to estimate the power of the intermod spurs for a certain mixer nonlinearity, i.e. given the IP₃ and 1-dB_c how much power will the spurs have? There are programs which can do such estimation. This would help in designing or chosing the proper mixer and/or the filter following the mixer.

Mixer using "Square-Law" Circuits

Next we will consider nonlinear circuits with second-order nonlinearity which are used to implement mixers. Assuming that the input is:

$$v_{in} = v_{RF} \cos(\mathbf{w}_{RF}t) + v_{LO} \cos(\mathbf{w}_{LO}t)$$

The output of the square-law circuit is given by:

$$v_{o} = \mathbf{a}_{1} [v_{RF} \cos(\mathbf{w}_{RF}t) + v_{LO} \cos(\mathbf{w}_{LO}t)]$$
Fundamental
+ $\mathbf{a}_{2} [\{v_{RF} \cos(\mathbf{w}_{RF}t)\}^{2} + \{v_{LO} \cos(\mathbf{w}_{LO}t)\}^{2}]$ SquareTerms
+ $2\mathbf{a}_{2} v_{RF} [\{\cos(\mathbf{w}_{RF}t)\} \{\cos(\mathbf{w}_{LO}t)\}]$ Cross Product

The square terms produce the DC and 2nd harmonics spectral components, while the cross product term can be rewritten as:

$$v_{crossprod} = \alpha_2 v_{RF} v_{LO} \left[cos(\omega_{RF} - \omega_{LO})t + cos(\omega_{RF} + \omega_{LO})t \right]$$

After filtering the sum term, the DC and the harmonics, we are let with the desired difference term. It is obvious that the undesired signal are distant from the IF and hence can be easily filter out. *This makes the square-law circuits more useful than circuits with higher order nonlinearity.*

Since the amplitude of the LO is constant ($v_{LO}=constant$). The output will be proportional to the RF signal and is given by:

$$v_o = \boldsymbol{a}_2 v_{RF} v_{LO} [\cos(\boldsymbol{w}_{RF} - \boldsymbol{w}_{LO})t]$$

The conversion given can now be easily derived as

$$G_c = \frac{\boldsymbol{a}_2 v_{RF} v_{LO}}{v_{RF}} = \boldsymbol{a}_2 v_{LO}$$

Notice that mixer with nonlinear circuits can be implemented using *two-port networks*.



A good approximation for a square-law device is a MOSFET (with long enough channel). Two examples of MOSFET based mixers are shown below. It can be shown that the (voltage) conversion gain is



Passive vs Active Mixers

Passive mixers provide no power gain (but may have either current *or* voltage gain), instead they have conversion loss.



Passive Mixer



Active Mixer

Active mixers provide power gain using active devices.

The advantage of active mixers is that its gain reduces the contribution of the noise of the subsequent blocks.

The advantages of passive mixers over active mixers is the superior linearity and speed.



Example: Active Mixer

Multiplier-Based Mixers

These can be classified as *single-balanced and double-balanced* mixers. Both implementations, which will be introduced next, are current mode. First, we will consider, single-ended and differential single-balanced mixers

Unbalanced Mixers

The RF signal is converted to a current signal using a transconductance. This current is switched to the IF port via a switch-wave signal. The output IF current is switching between I_{RF} and zero at the LO frequency. Therefore, it can be expressed, assuming 50% duty cycle, as:



$$_{IF} = (I_{BIAS} + gmv_{RF}) \times \left[0.5 + \frac{2}{p} \cos w_{LO}t + \frac{2}{3p} \cos 3w_{LO}t + \frac{2}{5p} \cos 5w_{LO}t + \cdots \right]$$



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Natural Sampling



For a 50% duty cycle the images will appear at the odd multiples of f_s , and the amplitude of the images is given by:

- A/2 f=0
- $2A(n\pi)$ f=nf_s where n=1,2,5,7, ...



The current at the IF-port switches between I_{RF} and zero.

Assuming that $v_{RF} = V_{RF} \cos \omega_{RF} t$, the current at the IF port becomes

$$\begin{split} I_{IF} &= \frac{I_{BIAS}}{2} \bigg[1 + \frac{4}{\pi} \cos \omega_{LO} t + \frac{4}{3\pi} \cos 3\omega_{LO} t + \frac{4}{5\pi} \cos 5\omega_{LO} t + \cdots \bigg] + \frac{gm}{2} V_{RF} \cos \omega_{RF} t \\ &+ gm V_{RF} \Biggl[\frac{1}{\pi} \cos(\omega_{LO} - \omega_{RF}) t + \frac{1}{\pi} \cos(\omega_{LO} + \omega_{RF}) t \\ &+ \frac{1}{3\pi} \cos(3\omega_{LO} - \omega_{RF}) t + \frac{1}{3\pi} \cos(3\omega_{LO} + \omega_{RF}) t \\ &+ \frac{1}{5\pi} \cos(5\omega_{LO} - \omega_{RF}) t + \frac{1}{5\pi} \cos(5\omega_{LO} - \omega_{RF}) t \cdots \bigg] \end{split}$$

The above expression shows several spectral components at :

- DC
- $n \mathbf{w}_{LO}$ where $n = 1, 3, 5 \cdots$
- $n\mathbf{w}_{LO} \pm \mathbf{w}_{RF}$ where $n = 1, 3, 5, \cdots$
- **W**_{RF}



The desired signal is the component at w_{RF} - w_{LO} . Its amplitude is

$$I_{IF}\Big|_{\boldsymbol{w}=\boldsymbol{w}_{RF}-\boldsymbol{w}_{LO}} = \frac{gm}{\boldsymbol{p}}V_{RF}$$

The conversion transconductance is

$$G_c = \frac{gm}{p}$$

The disadvantages of the unbalanced topology are:

• LO-IF feedthrough,

A spectra component at ω_{LO} appears at the IF-port. If the LO frequency is not far enough from the desired RF, it may be difficult to attenuate the LO component enough via filtering.

• RF-IF feedthrough (or *direct feedthrough*).

An RF spectra components shows at the IF-port. Direct feedthrough worsens the NF of the mixer because it allows the noise at the RF-port at the desired IF frequency to leak to the IF-port.

Single-Balanced Mixers



The mixer process is similar to that of unbalanced mixer, except that now the output IF current is switching between I_{IF} and $-I_{RF}$ at the LO frequency. Therefore, it can be expressed as:

$$I_{IF} = (I_{BIAS} + gm v_{RF}) \times \left[\frac{4}{\pi}\cos\omega_{LO}t + \frac{4}{3\pi}\cos3\omega_{LO}t + \frac{4}{5\pi}\cos5\omega_{LO}t + \cdots\right]$$

Notice that the difference between this equation and that of the unbalanced is the DC term (between the square brackets). In the single-balanced the IF current is switching between two "almost" equal levels with opposite signs, so the average is zero. The amplitude of the spectral components of the square wave (terms of the series) has doubled, again, because the IF current is switching between $\pm/-I_{RF}$. While, in the unbalanced mixer, the current swing is halved.



On phase #1, $I_{IF} = I_{BIAS} + gm v_{RF}$, while, on the 2nd phase, the current is equal to that of phase #1 but with opposite sign.

Again, assuming that $v_{RF} = V_{RF} \cos \omega_{RF} t$, the current at the IF port becomes

$$I_{IF} = I_{BIAS} \left[2 + \frac{4}{\pi} \cos \omega_{LO} t + \frac{4}{3\pi} \cos 3\omega_{LO} t + \frac{4}{5\pi} \cos 5\omega_{LO} t + \cdots \right]$$

+ $gmV_{RF} \left[\frac{2}{\pi} \cos(\omega_{LO} - \omega_{RF})t + \frac{2}{\pi} \cos(\omega_{LO} + \omega_{RF})t + \frac{2}{3\pi} \cos(3\omega_{LO} - \omega_{RF})t + \frac{2}{3\pi} \cos(3\omega_{LO} + \omega_{RF})t + \frac{2}{5\pi} \cos(5\omega_{LO} - \omega_{RF})t + \frac{2}{5\pi} \cos(5\omega_{LO} - \omega_{RF})t + \frac{2}{5\pi} \cos(5\omega_{LO} - \omega_{RF})t + \cdots \right]$
Spectral Components of the IF
 $\int_{RF} \int_{RF} \int_{RF}$

The spectral components at the IF port of the unbalanced single-balanced mixer are similar to those of the unbalanced mixers except that the RF component is suppressed (no RF to IF feedthrough. All other components are increased by 6dB. The conversion transconductance is:

$$G_c = \frac{2gm}{p}$$
¹⁹

Double-Balanced Mixers



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As shown in the figure, the double-balanced mixer uses two voltage dependent current sources. One is controlled by v_{RF} and the other is controlled by $-v_{RF}$. In additions, both current sources carry equal bias current. The current at the IF-port switches between gm v_{RF} (when the switches are connected to pints A and D) and $-gm v_{RF}$ (when the switches are connected to pints A and D) and $-gm v_{RF}$ (when the switches are connected to pints A and D) and $-gm v_{RF}$ (when the switches are connected to pints B and C). Note that the DC biasing current I_{BIAS} is canceled out since it flows through both load resistors at all times. The differential output voltage v_{IF} is thus not a function of the biasing current. The current at the IF-port can be expressed as:

$$I_{IF} = gmV_{RF} \begin{bmatrix} \frac{2}{\pi} cos(\omega_{LO} - \omega_{RF})t + \frac{2}{\pi} cos(\omega_{LO} + \omega_{RF})t \\ + \frac{2}{3\pi} cos(3\omega_{LO} - \omega_{RF})t + \frac{2}{3\pi} cos(3\omega_{LO} + \omega_{RF})t \\ + \frac{2}{5\pi} cos(5\omega_{LO} - \omega_{RF})t + \frac{2}{5\pi} cos(5\omega_{LO} - \omega_{RF})t + \cdots \end{bmatrix}$$



From the above expression we can make the following observation :

- the double balanced mixer has no sprectal components at the nw_{LO} where n = 1,3,5,... This implies that the LO IF feedthrough is significantly better than that of the single balanced mixer (the LO IF feedthrough is 40 to 60dB for IC realization).
- The RF IF feedthrough is theoretically nonexistent.
- While the amplitude of the spectral components of the IF current are equal for both the double - balanced and differential single - balanced mixers, the differential voltage at the IF - port (v_{RF}) of the double - balanced mixer is twice that of the differential single - balanced mixer.
- The conversion transconductance of the double balanced mixer equal to that of the differential single balanced mixer.

$$G_c = \frac{2gm}{p}$$



Implementation of Mixers

The most straightforward way to implement single- and double- balanced mixers is to use a current –mode (differential pairs).





MOS Double-Balanced Mixer



BJT Double-Balanced Mixer (Gilbert Multiplier) As we have seen before the conversion transconductance and, hence, the conversion gain depends on the device transconductance of the BJT or MOSFET used to convert the RF voltage signal to a current signal. It is, therefore, very important to maintain a linear device transconductance to avoid distortion. Several techniques have been proposed and employed to achieve that. Some of the linearization technique will be introduced next.



Common-Gate Linearization Common-Source Linearization Inductive Degeneration Linearization



• Common-Gate Linearization:

The transconductance of the common-gate at the RF port is

$$Gm = \frac{I_{M_{RF}}}{V_{RF}} = \frac{gm}{1 + gmR_1} \cong \frac{1}{R_1}$$

The above approximation is possible if $gm R_1 >> 1$. This can be achieved since the matching is not determined by gm only. The transistor transconductance gm can be made large enough, while R_1 will be close to $R_s(R_s = R_1 + 1/gm)$.

The disadvantage is the additional noise due to R_1 .

The conversion transconductance is $G_c = 2Gm/p$

• Common-Source Linearization:

The common-source will not match the source resistance which would be the output resistance of the image reject filter (50 Ω) for superheterodyne receivers.

• Inductive Degeneration:

This is the preferred topology since it allows for matching, linearization without additional noise. Furthermore, the DC drop across the inductor is zero which makes this topology suitable for low-voltage application. It can be shown that effective transconductance, at the frequency which corresponds to matching for max power transfer, is given by:

$$Gm = \frac{gm}{gm\sqrt{\frac{L_1}{C_{gs_{MRF}}} + R_S\sqrt{\frac{C_{gs}}{L_1}}}} = \sqrt{\frac{C_{gs_{MRF}}}{L_1}}$$

Double-Balanced Mixer



The inductive degeneration can be used in the double-balanced mixer, by adding source inductances. The biasing tail current is replaced by a tank whose resonance frequency is chosen to reject the common-mode components.

The conversion gain is similar to that of the single-balanced mixer.

Schmook's Linearization Multi-tanh Technique



$$i_{RF_1} - i_{RF_2} = i_{x1} + i_{y1} - i_{x2} - i_{y2}$$

= $(i_{x1} - i_{x2}) + (i_{y1} - i_{y2})$
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Passive CMOS Mixers

The advantage of passive mixers is its low-power consumption. It also may have superior linearity compared to active mixers.

Multiplier-based mixers using switching can be best implemented in CMOS technology.

A simple differential single-balanced mixer is shown below.

The following observations are due:

- If the swing of the LO voltage is large so that the gate-tosource overdrive is large enough the channel on-resistance is almost independent on the RF voltage signal. The mixer is, therefore, linear. If, however, the LO swing is small (which is the case for low-voltage applications) the linearity is degraded.
- The conversion voltage gain of this passive mixer is 2/π (-3.9dB) (assuming 50%-duty cycle square-wave LO voltage). As expected passive mixer have conversion gain less than unity.
- If the transistor width is increased to reduce the on-resistance, the capacitive LO-IF feedthrough increases.









Double-Balanced Passive CMOS Mixers

The advantage of passive mixers is its low-power consumption. It also may have superior linearity compared to active mixers.

Multiplier-based mixers using switching can be best implemented in CMOS technology.

A simple double-balanced mixer is shown below.



Subsampling Mixers

Subsampling the RF or IF signal is a way to downconvert and simultaneously sample for A/D operation. Subsampling RF is still too difficult to achieve in practice. The sampling rate has to be twice the information bandwidth, which is typically much smaller than the IF frequency.



Advantage: is the high linearity

Disadvantages: Jitter noise and thermal noise folding

The overall dynamic range is still inferior compared to other mixers.



 $\therefore N_o = \frac{N_i G}{2}$



Assume Image Noise is Filtered





Noise Factor Definitions for Mixers

Case I: SSB NF

One sideband of the signal is down converted and assume that the noise in the image frequency range is not filtered.

$$\mathbf{P} \quad S_o = \frac{S_i}{4}G$$

$$N_o = \frac{N_i G}{2}$$

$$F_{ssB} = \frac{S_i}{N_i} \times \frac{\text{No Total}}{S_o} = \frac{S_i}{N_i} \times \frac{N_o + N_{no}}{S_o}$$

$$= \frac{S_i}{N_i} \times \frac{\frac{N_{iG}}{2} + N_{no}}{\frac{S_i G}{4}} = 2 + \frac{4N_{no}}{GN_i}$$

Case II : DSB NF with S_{iA} and S_{iB} not correlated.

$$S_{o} = \frac{S_{i}}{2} G$$

$$N_{o} = \frac{N_{i}}{2} G$$

$$F_{DSB} = \frac{S_{i}}{N_{i}} \times \frac{N_{o} + N_{no}}{S_{o}}$$

$$= \frac{S_{i}}{N_{i}} \times \frac{\frac{N_{i}}{2} G + N_{no}}{\frac{S_{i}}{2} G} = 1 + \frac{2N_{no}}{GN_{i}}$$

Notice that

$$F_{DSB} = \frac{1}{2} F_{SSB}$$

DSB noise figure is 3dB less than the SSB noise figure.

Zero-IF is a typical example of a DSB case.

In practice, the image noise (in a SSB case) is filtered out using an IMR filter. The filter will not suppress the noise completely. Hence, we may assume that image noise will see a conversion gain of G_{imap} (= IMR less x G at image frequency). In such case



Noise in RF-CMOS Mixers

Ref: H. Darabi et. al. JSSC, Jan. 2000



The noise is contributed by:

- 1. Loads
- 2. Transconductance FETs
- 3. Switches



Load Noise:

The noise of the load, especially the 1/f noise, will be critical for zero-IF downconversion mixers.

Transconductance Noise:



The input referred noise will be translated in frequency in a similar fashion as the RF-input signal.



In the case of zero-IF, the white moise at f_{LO} , $3f_{LO}$, $5f_{LO}$, . . . (odd harmonics of LO) will be down converted to DC. The 1/f noise will be up converted to f_{LO} and its odd harmonics.



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Direct Switch Noise

The following assumptions will be made:

- The 1/f noise of both switches M1 and M2 will be referred to the input of one of the devices. It is a slow varying signal compared to the LO signal.
- The g_m of the differential pair is high.



Single-balanced mixer with switch Noise modeled at gate.



Assumed switch *I-V* characteristic.

Assume that all input phase noise is reflected to one side of the differential pair and is given by:

$$v_{id} = v_n(t) + 2A \sin \omega_{LO} t$$

Each time the V_{id} crosses zero the i_{od} switches from –I to I or vice-versa.

If $v_n(t)$ was ten the zero crossings of i_{od} would occur at exactly T/2 intervals (when T=1/f_{LO}).

If $v_n(t)$ causes the zero crossings to shift right or left. The current waveform can be divided into two waveforms.

• One ideal 50% duty-cycle square wave (due to the ideal sin \mathbf{w}_{LOL}

And its height = 2 I

Its frequency = $2 f_{LO}$

• Random current pulses due to $v_n(t)$. The width of each puse is $\Delta t = \frac{v_n(t)}{\Delta t}$

s Slope of LO @ zero 2 ωA



The current noise pulses have an averaging (over 1 T/2) of:

$$i_{o,n_{av}} = \frac{2I_o t}{T/2} = 2I \frac{v_n(t)}{s} \times \frac{2}{T} = \frac{I v_n}{\pi A}$$
$$2\pi f A \ge 2$$

The current noise pulses can be approximated by delta-function impulses (since $\Delta t \ll T$).

This is similar to a sampling process. Hence, the spectrum, due to the 1/f noise of the switches, at the output looks like:



Mixer output spectrum in presence of direct noise.

There are images (alias) of 1/f noise at DC, 2 f_{LO} , 4 f_{LO} , ... (harmonics of 2 f_{LO}).

SNR due to the switch noise can be derived as follows:

$$SNR_{1} = \frac{\text{signal}}{\text{noise}} = \frac{i_{o}}{i_{no}}$$

$$signal = \text{conversion } G_{m} \times V_{in} = \frac{2g_{m}}{\pi} V_{in}$$

$$SNR_{1} = \frac{2g_{m}v_{in}}{\pi} \cdot \frac{1}{\frac{1}{\pi} \frac{IV_{n}}{A}} = \frac{2A}{V_{GS} - V_{T}} \cdot \frac{v_{in}}{v_{n}}$$

$$g_{m} = \frac{I}{V_{GS} - V_{T}} \quad \text{for short channel MOS}$$

$$\longrightarrow \text{This implies that SNRs can be improved by}$$

$$- \text{increasing A through LO swing}$$

$$\text{Trade off} \quad - \text{reducing } V_{gs} \cdot V_{T} (\text{over drive of the switches})$$

with frequency - reducing N_n by increased WL of the switch.

For the double-balanced mixer, the previous analysis is pretty much the same.

The main differences are:

- No LO feedthrough
- v_n would be the noise of all four switches.

White Noise in Mixer Switches:

The approach used for 1/f noise (on pp 5-8) can be extended to the white noise. By referring the white noise to the input of the switches as in the figure of pp 5. The white noise results in train of pulses at $2f_{LO}$ (as shown below). To simplify the analysis, the random current pulses is approximated by a train of rectangular pulses with a constant width (Ts) and a height of $2I/ST_s$. Then pulses sample the noise v_n . This imples that the white noise will be down converted to IF (or DC for zero – IF). The noise which will be down-converted, is at $2f_{LO}$ and its harmonics. This is a kind of subsampling.

Transconductor White Noise

The white noise of Gm device, when referred to the input, can be treated the same way as the (RF) input signal. Assuming a 50% duty cycle (square wave) LO, the white noise at $f_{LO} + IF$, $3f_{LO} + IF$, $5f_{LO} + IF$, ... will all be down converted to IF.

